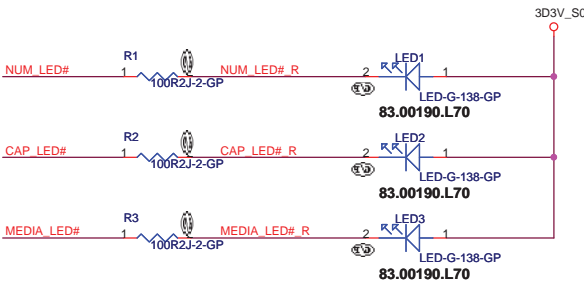
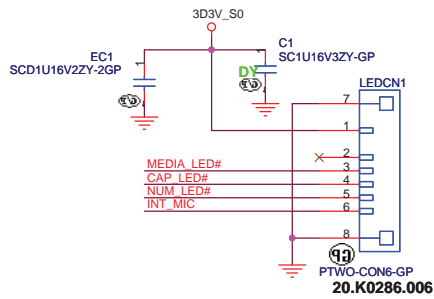
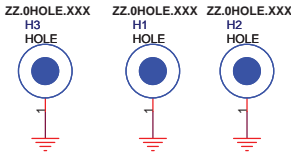
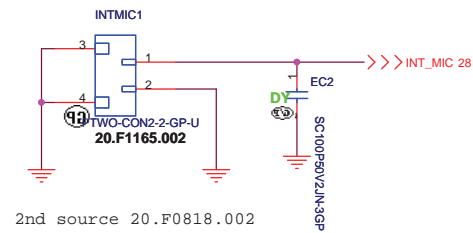


Cathedral Peak LED Board

Project code: 91.4J501.001
PCB P/N : 48.4J503.0SB
REVISION : 07950-SB



Analog Int. Mic



<Variant Name>		
緯創資通 Wistron Corporation		
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Title LED_Board		
Size A3	Document Number	Rev SB
Cathedral Peak		
Date: Thursday, March 06, 2008	Sheet 1 of 1	